

PATENT NUMBER

U.S. **UTILITY** Patent Application

**PATENT DATE**

SCANNED 1 M 2 Q.A.

APPLICATION NO. 09/933532	CONT/PRIOR D	CLASS <del>327</del> 703	SUBCLASS 18	ART UNIT <del>2816</del> 2123	EXAMINER GARCIA-OTERO
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Farid Najm  
Richard Shank

Apparatus and methods for characterizing electronic circuits having multiple power supplies

PTO-2040  
12/99

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	<b>Sheets Drwg.</b>	<b>Figs. Drwg.</b>	<b>Print Fig.</b>	<b>Total Claims</b>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Assistant Examiner)		<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Date)			
	_____ (Primary Examiner)		<b>ISSUE FEE</b>	
	_____ (Date)		<b>Amount Due</b>	<b>Date Paid</b>
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		<b>ISSUE BATCH NUMBER</b>	
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